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# TRANSMITTAL **FORM**

(To be used for all correspondence after initial filing)

Application Number	10/090,978	
Filing Date	March 4, 2002	
First Named Inventor	Federico Pio	
Group Art Unit	2814	
Examiner Name	Douglas A. Wille	
Attorney Docket No.	856063.616D1	

ENCLOSURES (check all that apply)			
Fee Transmittal Form Fee Attached Amendment/Response After Final Affidavits/declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statement; Form PTO-1449 Cited References Certified Copy of Priority Document(s) Response to Missing Parts under 37 C.F.R. 1.52 or 1.53 Response to Missing Parts/Incomplete Application	Assignment Papers (for an Application)  Drawing(s) Request for Corrected Filing Receipt Licensing-related Papers Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation, Change of Correspondence Address Declaration Statement under 37 CFR 3.73(b) Terminal Disclaimer Small Entity Statement Request for Refund	CD(s), Number of CD(s)  After Allowance Communication to Group  Appeal Communication to Board of Appeals and Interferences  Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)  Proprietary Information  Status Letter  Return Receipt Post Grd  Additional Enclosure(s) (please identify below):  ARR 214	
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Date March 18, 200	03		
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## EXPRESS MAIL NO. EV207744587US PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicants** 

Federico Pio et al.

Application No.

10/090,978

Filed

March 4, 2002

For

IMPROVED FIELD-EFFECT TRANSISTOR AND

CORRESPONDING MANUFACTURING METHOD

Examiner

Douglas A. Wille

Art Unit

2814

Docket No.

856063.616D1

Date

March 18, 2003

MAR 24 2003 TECHNOLOGY CENTER 2800

Box Non-Fee Amendment Commissioner for Patents Washington, DC 20231

#### **AMENDMENT**

#### Commissioner for Patents:

In response to the Office Action dated December 18, 2002, please amend the application as follows:

### In the Claims:

## Please amend claims 1-3 and 6-11 to read as follows:

- 1. (Amended) A field effect transistor integrated on a semiconductor substrate having an active area, the field effect transistor comprising:
  - a source region and a drain region formed in the semiconductor substrate;
- a channel region interposed between said source and drain regions having a predefined nominal width in a first direction that is perpendicular to a second direction that

